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APPLICANTS Bernd Deutschmann, Munich, GERMANY; Bernd Frankhauser, Graz, AUSTRIA; Michael Mayerhofer, Graz, AUSTRIA; Pawel Chojecki, Graz, AUSTRIA;							
** CONTINUING DATA ***** This application is a 371 of PCT/EP05/01476 02/14/2005							
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35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No		GERMANY	4	21	2	
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TITLE Circuit Arrangement and Method For Protecting an Integrated Semiconductor Circuit							
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